

| Ref # | Hits | Search Query   | DBs                                 | Default Operator | Plurals | Time Stamp       |
|-------|------|--|-------------------------------------|------------------|---------|------------------|
| L1    | 1    | "20040135951"  | US-PGPU B; USPAT                    | OR               | OFF     | 2005/04/07 16:05 |
| L2    | 1    | "20010054709"  | US-PGPU B; USPAT                    | OR               | OFF     | 2005/04/07 16:06 |
| L3    | 1    | "20020130311"  | US-PGPU B; USPAT                    | OR               | OFF     | 2005/04/07 16:06 |
| L4    | 1    | "20020130363"  | US-PGPU B; USPAT                    | OR               | OFF     | 2005/04/07 16:06 |
| L5    | 1    | "20020130353"  | US-PGPU B; USPAT                    | OR               | OFF     | 2005/04/07 16:07 |
| L6    | 70   | stumbo-d\$.in.   | US-PGPU B; USPAT; EPO; JPO; DERWENT | OR               | OFF     | 2005/04/07 16:07 |
| L7    | 11   | l6 and nanowire  | US-PGPU B; USPAT; EPO; JPO; DERWENT | OR               | OFF     | 2005/04/07 16:31 |
| L8    | 0    | (active adj matrix or lcd) and (nanowire near2 transistor) | US-PGPU B; USPAT                    | OR               | ON      | 2005/04/07 16:37 |
| L9    | 2    | (active adj matrix or lcd) and (nanowire near2 transistor) | US-PGPU B; USPAT                    | OR               | ON      | 2005/04/07 16:35 |
| L10   | 7    | (active adj matrix or lcd) and (nano near2 transistor)     | US-PGPU B; USPAT                    | OR               | ON      | 2005/04/07 16:36 |
| L11   | 0    | (active adj matrix or lcd) and (nano-transistor)           | US-PGPU B; USPAT                    | OR               | ON      | 2005/04/07 16:36 |
| L12   | 0    | (active adj matrix or lcd) and (nanotransistor)            | US-PGPU B; USPAT                    | OR               | ON      | 2005/04/07 16:36 |

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| L13 | 2  | (active adj matrix or lcd)<br>and (nanopixel or<br>nano-pixel) | US-PGPU<br>B; USPAT                                    | OR | ON | 2005/04/07<br>16:37 |
| L14 | 32 | (nanowire or nano-wire)<br>near2 transistor                    | US-PGPU<br>B; USPAT                                    | OR | ON | 2005/04/07<br>16:40 |
| L15 | 0  | l14 and "345"/\$.ccls.   | US-PGPU<br>B; USPAT                                    | OR | ON | 2005/04/07<br>16:38 |
| L16 | 2  | l14 and lcd  | US-PGPU<br>B; USPAT                                    | OR | ON | 2005/04/07<br>16:38 |
| L17 | 5  | (nanowire or nano-wire)<br>near2 transistor                    | EPO;<br>JPO;<br>DERWEN<br>T                            | OR | ON | 2005/04/07<br>16:43 |
| L18 | 0  | "0217362".pn.  | EPO;<br>JPO;<br>DERWEN<br>T                            | OR | ON | 2005/04/07<br>16:42 |
| L19 | 22 | "0217362"  | EPO;<br>JPO;<br>DERWEN<br>T                            | OR | ON | 2005/04/07<br>16:42 |
| L20 | 50 | nano\$ near5 wire near5<br>transistor                          | US-PGPU<br>B;<br>USPAT;<br>EPO;<br>JPO;<br>DERWEN<br>T | OR | ON | 2005/04/07<br>17:00 |
| L21 | 1  | l20 and lcd  | US-PGPU<br>B;<br>USPAT;<br>EPO;<br>JPO;<br>DERWEN<br>T | OR | ON | 2005/04/07<br>16:44 |
| L22 | 13 | l20 and display  | US-PGPU<br>B;<br>USPAT;<br>EPO;<br>JPO;<br>DERWEN<br>T | OR | ON | 2005/04/07<br>17:01 |

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| L23 | 3   | l20 and microwire   | US-PGPU<br>B;<br>USPAT;<br>EPO;<br>JPO;<br>DERWEN<br>T | OR | ON | 2005/04/07<br>17:19 |
| L24 | 284 | tft same nano\$   | US-PGPU<br>B;<br>USPAT;<br>EPO;<br>JPO;<br>DERWEN<br>T | OR | ON | 2005/04/07<br>17:04 |
| L25 | 13  | tft same nano\$ near3<br>transistor                               | US-PGPU<br>B;<br>USPAT;<br>EPO;<br>JPO;<br>DERWEN<br>T | OR | ON | 2005/04/07<br>17:39 |
| L26 | 1   | 345/87-100.ccls. and<br>(nano\$ near3 transistor)                 | US-PGPU<br>B; USPAT                                    | OR | ON | 2005/04/07<br>17:10 |
| L27 | 1   | 345/204.ccls. and (nano\$<br>near3 transistor)                    | US-PGPU<br>B; USPAT                                    | OR | ON | 2005/04/07<br>17:11 |
| L28 | 7   | "345"/\$.ccls. and (nano\$<br>near3 transistor)                   | US-PGPU<br>B; USPAT                                    | OR | ON | 2005/04/07<br>17:12 |
| L29 | 38  | (display same matrix) and<br>(nano\$ near3 transistor)            | US-PGPU<br>B; USPAT                                    | OR | ON | 2005/04/07<br>17:13 |
| L30 | 24  | l20 and micro\$4  | US-PGPU<br>B;<br>USPAT;<br>EPO;<br>JPO;<br>DERWEN<br>T | OR | ON | 2005/04/07<br>17:28 |
| L31 | 14  | (nano\$4 near3 transistor)<br>same (micro\$4 near3<br>transistor) | US-PGPU<br>B;<br>USPAT;<br>EPO;<br>JPO;<br>DERWEN<br>T | OR | ON | 2005/04/07<br>17:29 |

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| L32 | 3  | (carbon adj nano-tube)<br>same (nanowire or<br>nano-wire)                   | US-PGPU<br>B;<br>USPAT;<br>EPO;<br>JPO;<br>DERWEN<br>T | OR | OFF | 2005/04/07<br>17:43 |
| L33 | 97 | (tft or field adj effect adj<br>transistor) same (nanowire<br>or nano-wire) | US-PGPU<br>B;<br>USPAT;<br>EPO;<br>JPO;<br>DERWEN<br>T | OR | ON  | 2005/04/07<br>17:44 |